



राष्ट्रीय प्रौद्योगिकी संस्थान वरंगल

NATIONAL INSTITUTE OF TECHNOLOGY WARANGAL

(शिक्षा मंत्रालय, भारत सरकार के अधीन राष्ट्रीय महत्व का एक संस्थान)

(An Institute of National Importance under Ministry of Education, Govt. of India)

तेलंगाना / Telangana – 506004, India.

ADVERTISEMENT FOR JUNIOR RESEARCH FELLOW (JRF) RECRUITMENT

Under ANRF Research Funded Project

Project title	I-HPEV: Interactive Hybrid Power Supply System for Grid and EV Charging Application
Sponsoring agency	ANRF- IRG SCHEME
Position title	Junior Research Fellow (JRF)
Duration (in years)	Three years or till closure of the project whichever is earlier.
Number of position(s)	One
Essential qualification	<ol style="list-style-type: none"><i>B.Tech/BE in Electrical Engineering or Electrical & Electronics Engineering, or any equivalent specialization and</i><i>M.Tech/M.E in any of the following specializations: Power Electronics, Power Electronics and Drives, Electric Vehicle, Electric Transportation, Smart Mobility, Sustainable Systems/Renewable Energy, Energy Storage Technologies, Smart Electric Grid, Renewable Energy, Electrical Engineering, or any other specialization closely aligned with Power Electronics and Drives or a related field relevant to the project.</i><i>Consistent first-class academic record throughout all educational qualifications.</i> <p>Candidates will be given preference with</p> <ol style="list-style-type: none"><i>Qualification in GATE (Graduate Aptitude Test in Engineering) in Electrical Engineering (preferred, but not mandatory).</i><i>Prior experience in any Research & Development (R&D) laboratory or involvement in relevant academic/industry projects (preferred, but not mandatory).</i>
Desired qualifications	Prior knowledge in Power Electronics, Electric Vehicle, Electric Transportation, Renewable energy system integration, Mathematical Modelling, and simulation using MATLAB, PSIM, and PSCAD software. Candidate should have experimental knowledge in the hardware/control design of power converters. Familiarity with PCB Design Software and DSP/FPGA Controller. Good analytical ability and competent in report writing.
Age limit	30 years
Age relaxation	As per the norms and guidelines of the Institute
Emoluments	₹ 37,000/- per month for the first two years and ₹ 42,000/- per month for the third year
HRA/ Accommodation	HRA as per the institute rules. The selected candidate is also eligible for on-campus Hostel & Mess facilities, subject to availability
Job nature	Simulation and Hardware-based study
Other benefits	The selected candidate is eligible to register for a Ph.D. degree at NIT Warangal as per the institute rules. However, he/she needs to apply for a Ph.D. position when the advertisement is open
How to apply?	Complete the application in the prescribed format (as per the attached proforma with this) and submit it through the following Google Form link, along with a detailed bio-data/CV and scanned copies of all educational certificates. All documents, including the caste certificate (if applicable for age relaxation), must be combined and uploaded as a single PDF file. Google form link: https://forms.gle/Ga4cpZeSmRitxjru9
Submission mode	Online Application
Last date to apply	19/07/2026 @ 11.59pm
Whom to contact?	Dr. Ramulu Chinthamalla, Associate Professor, Department of Electrical Engineering, National Institute of Technology Warangal – 506 004 Telangana State, India Contact no: +91-8332969291, +91-9866561691
Contact Email ID	rmitchinthamalla@nitw.ac.in

Please note:

- The position is purely temporary
- Please enclose a brief resume/ curriculum vitae/ biodata along with the application form
- List of eligible candidates will be announced on the institute website/intimated via email or phone.
- Shortlisted candidate will have to attend an interview in OFFLINE mode only.
- No TA/DA will be paid for attending the interview

APPLICATION FOR THE JRF/SRF POSITION UNDER ANRF SPONSORED RESEARCH PROJECT

Advt. No. NITW/EE/2026/CHR/ANRF-IRG/-

(Please enclose a copy of biodata/ resume/ curriculum-vitae)

1. Name of the candidate

2. Father's name

3. Date of birth (DD/MM/YYYY)*

Contact No:

4. Sex Male/ Female

5. Marital status Single/ married

6. Category* OC/OBC/SC/ST

7. Educational qualifications*

Degree	Specialization/ subjects	Board/ University	Year of passing	Class	Percentage/ CGPA
SSC					
Intermediate					
B.Sc./ B.E./B.Tech					
M.Sc./ M.Tech.					
Ph.D.					

8. GATE/ NET/ ICMR/ DBT-SRF score earned/ qualified*

Score	Year appeared	Score	All India rank
GATE/ NET/ mention			

9. Research/Industrial/Teaching experience*

S.No.	Organisation/ industry served	Date/ Month of entry	Date/ Month of leaving	Months/ Years of experience

10. Publications/ conference participations*

11. Any other relevant information

Declaration

I hereby declare that all statements made in this application are true, complete and correct to the best of my knowledge and belief. In the event of the information being found false or incorrect or any ineligibility being detected before or after the selection, my candidature is liable to be cancelled. I confirm that I am available for ONLINE interview, if necessary.

Date:

Signature of the candidate

* Submit soft copies of the supporting documents