



राष्ट्रीय प्रौद्योगिकी संस्थान वरंगल
NATIONAL INSTITUTE OF TECHNOLOGY WARANGAL
(शिक्षा मंत्रालय, भारत सरकार के अधीन राष्ट्रीय महत्व का एक संस्थान)
(An Institute of National Importance under Ministry of Education, Govt. of India)
तेलंगाना / Telangana – 506004, India.

Department of Electronics and Communication Engineering

Advertisement for JRF recruitment under ANRF research project IRG

Project title	Design and Fabrication of 2D- Material Resistive Random Access Memory (RRAM) for Synaptic and Sensor Applications
Sponsoring agency	Anusandhan National Research Foundation (ANRF), Inclusivity Research Grant (IRG)
Position title	Junior Research Fellow (JRF)
Duration (in years)	3 Years
Number of position(s)	One
Emoluments	Candidate who are selected through National Eligibility Test (CSIR UGC NET or GATE) <ul style="list-style-type: none">• 37,000+HRA (1st and 2nd Year)• 42,000+HRA (3rd Year, upon successful assessment and upgradation to SRF) Candidate without National Eligibility Test (CSIR UGC NET or GATE) <ul style="list-style-type: none">• 30,000+HRA (1st and 2nd Year)• 33,000+HRA (3rd Year, upon successful assessment and upgradation to SRF)
Age limit	30 years
Age relaxation	The upper age limit is relaxable by up to 5 years for SC/ST/OBC, women, and persons with disabilities.
Essential qualification	Essential: Candidates with a minimum of 60% aggregate marks or 6.5 CGPA at both UG and PG for GEN/ GEN-EWS/ OBC-NCL category and minimum 55% aggregate marks or 6.0 CGPA at both UG and PG for SC/ ST/ PwD category. OR Candidates with Bachelor's degree in Engineering/ Technology with at least CGPA of 7.5/10 or 70% of marks under GEN/ GEN-EWS/ OBC-NCL category and at least CGPA of 7.0/10 or 65% under SC/ ST/ PwD category. B.Tech/ B.E or Equivalent degree in Electronics and Communication Engineering and M.Tech/ M.E in Electronics and Communication Engineering or equivalent with Research areas in Microelectronics/VLSI/ Integrated Circuits & Systems/Fabrication and Characterization. (OR) B.Tech/ B.E or Equivalent degree in Electronics and Communication Engineering.
Desired qualifications	A solid understanding of Semiconductor Physics, Semiconductor Device Modeling, Advanced MOS devices, Hands on TCAD Tools, Hands on fabrication & characterization of devices and sensors is crucial. Experience with programming languages and simulation tools such as MATLAB or Python is highly desirable.
HRA/ Accommodation	HRA will be provided as per institute rules. The selected candidate will also be eligible for on-campus hostel and mess facilities, subject to availability.
Job nature	Research and Development
Other benefits	The selected candidate will be eligible to register for a Ph.D. degree at NIT Warangal as per the institute rules. However, they must apply for the Ph.D. program when the official advertisement is released.
How to apply?	Complete the application (as proforma attached/link provided) and submit via google form along with the Bio-data/CV (one page) and scanned copy of certificates in a single pdf file.
Submission mode	Online Application
Last date to apply	10-07-2026 @11:59 pm
Whom to contact?	Prof. Vadthiya Narendar Associate Professor, Department of Electronics and Communication National Institute of Technology Warangal, 506004 Telangana, India
Contact Email ID	narendarv@nitw.ac.in

Please note:

- The position is purely temporary
- Please enclose a brief resume/ curriculum vitae/ bio-data along with the applications form
- Shortlisted candidate will have to attend an interview (ONLINE/OFFLINE)
- If interviews need to be conducted online, candidates are expected to have a good internet connection
- No TA/DA will be paid for attending the interview

The proforma of the application is attached to this advertisement. The editable template can be downloaded from [here](#).

APPLICATION FOR THE POSITION OF JRF
Department of Electronics and Communication
National Institute of Technology Warangal
(Please enclose a copy of bio-data/ CV and scan copy of certificates)

1. **Name of the candidate:**
2. **Father's name:**
3. **Date of birth (DD/MM/YYYY):**
4. **Sex:** Male/ Female
5. **Marital status:** Single/ married
6. **Category:** OC/OBC/SC/ST
7. **Educational qualifications:**

Degree	Specialization/ subjects	Board/ University	Year of passing	Class	Percentage/ CGPA
SSC					
Intermediate/ Diploma					
B.Sc./ B.E./B.Tech					
M.Sc./ M.Tech.					
Ph.D.					

8. GATE/ NET/ ICMR/ DBT-SRF score earned/ qualified

Score	Year appeared	Score	All India rank
GATE/ NET/ mention			

9. Research/Industrial/Teaching experience

S.No.	Organisation/ industry served	Date/ Month of entry	Date/ Month of leaving	Months/ Years of experience

10. Publications/ conference participations

11. Any other relevant information

Declaration

I..... hereby declare that all statements made in this application are true, complete and correct to the best of my knowledge and belief. In the event of the information being found false or incorrect or any ineligibility being detected before or after the selection, my candidature is liable to be cancelled. I confirm, that I am available for ONLINE interview, if necessary.

Date:

Signature of the candidate