राष्ट्रीय प्रौद्योगिकी संस्थान, उत्तराखण्ड

NATIONAL INSTITUTE OF TECHNOLOGY, UTTARAKHAND

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WALK-IN-INTERVIEW

(Advt. No. 11/2024; Date: 11.06.2024)

With reference to the advertisement No. 11/2024; dated 11.06.2024, Walk-In-Interview shall be conducted for the position of Junior Research Fellow for the project titled "A systematic Design of Robust Memory cell using Ferroelectric FET (FeFET) for 'Al Hardware Accelerator" under Core Research Grant (file Number: CRG/2023/007440) by SERB, DST Govt. of India, under the supervision of Dr. Nitanshu Chauhan, Co-Principal Investigator, Department of Electronics Engineering.

The details of post, essential qualifications and fellowship are as under: -

S No	Designation of post	Essential Qualifications	Fellow- ship	Number of posts	Tentative date and time of walk-in interview
1.	Junior Research Fellow	Essential Qualification: M.E/M. Tech./ MS/MSC or equivalent degree in Electronics Engineering/Electronics and Communication Engineering / Electronics and Tele Communication Engineering/ Electrical and Electronics Engineering/ Electrical Engineering and allied subjects with specialization in Microelectronics and VLSI or Equivalent with minimum of 1st class or equivalent CGPA and GATE/NET qualified in Electronics and Communication Engineering / Electrical engineering or in allied branch OR without gate but 2 years of research/industry experience.	Rs. 37,000 per month +8% HRA	01	6 th July, 2024 at 11 AM

Interested candidates shall appear for walk-in interview along with application form, all originals and self-attested photo-copies of relevant certificates as per the above schedule at Department of Electronics Engineering, NIT Uttarakhand, Srinagar Campus, Srinagar Garhwal - 246174. Candidates should send the advance copy of completely filled application form and relevant certificates as a single pdf file via email to nitanshu.chauhan@nituk.ac.in on or before 5th July 2024.

General Terms & Conditions:

- 1. The duration of post will be initially for one year, which may be extended up to the duration of the project based on the satisfactory performance. The fellowship may be terminated if performance of candidate is not satisfactory.
- 2. The post created under the SERB project is purely on temporary basis and neither SERB nor the Institute would have liability whatsoever for sustaining temporary staff recruited under the above-mentioned project, after its completion/termination.
- 3. Only those candidates who are present before the reporting time on scheduled date will be considered for Interview. Candidates reporting after the scheduled reporting time for document verification shall NOT be eligible to appear for the Interview.
- 4. The Junior Research Fellow with valid gate score or 2 years industry/research experience will have option to register for Ph.D. program of institute/part-time; the candidate must meet the essential qualifications and qualify written examination & interview of Ph.D. admission of institute.
- 5. NIT Uttarakhand has right to cancel the recruitment for the aforesaid post at any time without providing any clarifications/information.
- 6. No TA/DA will be paid for attending the Walk-In-Interview.

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DEPARTMENT OF ELECTRONICS ENGINEERING

Application form for the post of Junior Research Fellow in research project

1. Name in full	
2. Father's / Husband's name	Paste
3. Date of Birth	
4. Nationality	Passport size
5. Gender	Photo
6. Category	All Rest State
7. Mobile number	and account of the control of the co
8. Email id	
9. Address for correspondence:	

10. Permanent address:

11. Detailed educational qualification in chronological order starting from 10th standard:

Degree	Specialization	Name of University/Institute	% of Marks or CGPA obtained	Class/Division	Year of passing
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			7		
	à				

Details of exam	inations like	GATE/NET-JRF qu	ualified: -			
(Original certific	cates and Self	-attested photo-sta	ate copies shou	ıld be produce	ed at the time	of interview).
Name of Examination	Year of Passing	Validity Period	Number of candidates appeared	Marks obtained	AIR Rank	Score / Percentile

12. M. Tech. project title:

15. Research experience (if any) (max 200 words)

15. Publications (if any)

16. Any other relevant information (max. 100 words) **Declaration** I certify that the above information is correct and complete to the best of my knowledge and belief and nothing has been concealed /distorted. If at any time I am found to have concealed /distorted any material/ information, my engagement shall be liable to be summarily terminated without/notice/ compensation. Place: Date: (Signature of Candidate)