



**राष्ट्रीय प्रौद्योगिकी संस्थान श्रीनगर**  
**NATIONAL INSTITUTE OF TECHNOLOGY SRINAGAR**  
(An autonomous Institute of National Importance under the aegis of the Ministry of HRD, Govt. of India)  
**Hazratbal, Srinagar Jammu and Kashmir, 190006, INDIA**  
**Department of Electrical Engineering**

**Advertisement for the post of Research Assistant under JKST&IC,**  
**DST Sponsored Project**

Applications are invited from motivated, innovative and eligible candidates for the position of **Research Assistant (01)** to work on the project titled as **Design and Analysis of Combined Solar Photo-voltaic System and Wind Energy System using Variable Frequency Transformer (VFT) for Rural Electrification in J&K** funded by JKST&IC, DST. The selected person would work initially in the project for a period of eight months, which is extendable upto completion of research project, subject to satisfactory performance, conduct and availability of funds.

<b>Details</b>	
Name of the Position	Research Assistant (RA)
No. of Post	01 (One)
Essential Qualifications	<b>B. Tech.</b> and <b>M. Tech.</b> in Electrical Engineering or equivalent with minimum 60% aggregate marks (CGPA $\geq$ 6.5/10) to qualify under Open/OBC category and minimum 55% aggregate marks (CGPA $\geq$ 6.0) for candidates under SC/ST category. <p style="text-align: center;">OR</p> <b>B. Tech.</b> (4 years) in Electrical Engineering or equivalent with minimum 75% aggregate marks (CGPA $\geq$ 8.0/10) to qualify under Open/OBC category and minimum 70% aggregate marks (CGPA $\geq$ 7.5) for candidates under SC/ST category, and must have qualified GATE with valid score.
Desirable	Candidate should have sound knowledge of Renewable Energy, Drives and MATLAB/Typhoon HIL.
Emoluments	₹ 20,000/- (Consolidated)
Registration for PhD	The selected candidate can register for PhD programme if eligible, as per institute rules and statutes
How to Apply?	The filled application form (Proforma attached with advertisement) along with CV and scanned copy of the testimonials should be uploaded in the Google Form <a href="https://forms.gle/JxSA99j8msWQEXc6A">https://forms.gle/JxSA99j8msWQEXc6A</a> Incomplete Application forms will be rejected without any information.
Submission	Soft Copy via the above-mentioned Google Form
Last Date	<b>06-06-2025 (Friday)</b>
Whom to Contact?	<b>Dr. Farhad Ilahi Bakhsh</b> Principal Investigator (PI), Department of Electrical Engineering, National Institute of Technology Srinagar, Hazratbal-190006, Srinagar (J&K), India Email: <a href="mailto:farhad@nitsri.ac.in">farhad@nitsri.ac.in</a> Contact No. +91 9045300569 (M)



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## Note:

1. The selected candidate may be considered for admission to the PhD program, as per Institute eligibility, rules and statutes.
2. The position is purely temporary. The initial appointment will be for eight months, which is extendable upto completion of research project, subject to satisfactory performance, conduct and availability of funds.
3. At the time of joining, the selected candidate has to produce all relevant documents/testimonials in original for verification and a medical certificate of fitness from the medical officer of the institute. Failing this, the institute will offer the position to the next candidate on the merit list prepared for this purpose. CVs with incomplete information or without supporting documents will be summarily rejected.

## Note:

- CV should include contact details (address, mobile phone no., email ID), date of birth, qualifications, research experience, and publication details (if any).
- All the testimonials (scanned copies of mark sheets, degree certificates, experience certificates, GATE certificates, valid identity proof, etc.) in pdf as a single compressed (zip/RAR format) file must be uploaded in the above-mentioned Google form.

## Screening & Interview Process

Candidates will be shortlisted based on their merit and as per the requirements of the project. Mere qualification of eligibility criteria does not guarantee shortlisting. The interview shall be tentatively conducted in the month of June 2025. The date, time, and venue of the interview will be intimated to short-listed candidates via email. **No TA/DA will be paid for attending the interview.** Candidates who are already employed should produce a 'No Objection Certificate' (NOC) from the present employer.

For any further clarifications, candidates may contact:

**Dr. Farhad Ilahi Bakhsh**

Principal Investigator (PI),

Department of Electrical Engineering,

National Institute of Technology Srinagar,

Hazratbal-190006, Srinagar (J&K), India

Email: [farhad@nitsri.ac.in](mailto:farhad@nitsri.ac.in)

Contact No. +91 9045300569 (M)

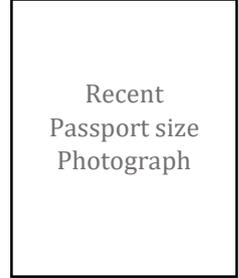


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**Application Form for Research Assistant (RA)**

**Project ID: JKST&IC/SRE/211-15 of JKST&IC, DST Sponsored Project**

1. Name of the Candidate:.....
2. Parentage: .....
3. Date of Birth (DD/MM/YYYY): .....
4. Gender: .....
5. Marital Status: .....
6. Category (SC/ST/PwD/OBC/Open):.....
7. Academic Records:



(a) Qualifying Degree Details:

Degree	University / Board	Grade/ Division	CGPA/ %age	Year of Passing	Branch/ Specialisation/ Main Subject(s)	Remarks
M.Tech						
B.Tech						
12 <sup>th</sup> / Intermediate						
10 <sup>th</sup>						

(b) GATE Score:

Exam	Year	Scores	All India Rank	Validity Up to
GATE				

8. Having working knowledge of Renewable Energy Systems? (Yes or No): .....
9. Having working knowledge of MATLAB/ Typhoon HIL? (Yes or No): .....
10. Whether interested for Ph.D. in Electrical Engineering? (Yes or No): .....
11. Publications/Conference participations (use additional sheet, if required):
12. Corresponding Address: .....

Mobile No. :..... E-mail ID:.....

13. Any other relevant information: .....
14. **Declaration:** I hereby declare that the information provided herewith is correct to the best of my knowledge and belief and nothing has been concealed. If any information at any stage is found to be incorrect then I shall be liable for action taken by the authority as per existing rules/law.

Date: .....

(Signature of Candidate)