

22 December 2025

**Notification for Recruitment of Junior Research Fellow, Electrical Engineering**  
**Project funded by the ANRF ARG**  
**Ref: IITPKD/2025/054/EE/MAJ**

- **Project Areas:** Design and development of a reliable power swing detection scheme for inverter based resource integrated transmission system.
- **Required Skills:** The candidate must possess a strong understanding of power system modeling and simulation. Familiarity with as many of the following software packages as possible: PSS/E, DSATools, PSCAD, Matlab. Familiarity with C/C++/Matlab programming is desired.
- **Eligibility:** B.E./ B.Tech with a valid GATE score or M.E./ M.Tech from reputed institutions with specialization in areas allied to the advertised project areas.
- **Age:** Candidates who are not exceeding 31 years of age, as on the closing date of application, with relaxation to candidates belonging to OBC/ SC/ ST/ PWD categories and women applicants as per Government of India norms.
- **Appointment Duration:** Initially for a period of one year, with a further extension based on the availability of funds and the performance of the candidate.
- **No. of Positions:** One
- **Salary Particulars:** The monthly remuneration for this position will be Rs. 37,000/- (Thirty-Seven Thousand Rupees Only). Hostel accommodation may be provided at the institute based on availability on a chargeable basis. In case of non-availability of institute hostel, admissible HRA will be provided as per the Government of India rules.
- **Deadline for application:** 15 January 2026 (Thursday)

Candidates satisfying the required skills may send their resumes and certificates pertaining to educational qualifications starting from class X (in a single PDF) to “[mkj@iitpkd.ac.in](mailto:mkj@iitpkd.ac.in)”. The subject of the application e-mail should be “Application for Junior Research Fellow - **IITPKD/2025/054/EE/MAJ**”. The candidate is expected to join immediately after receiving the offer letter.

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