

**osition:** Junior Research Fellowship  
**PI:** Shailendra Kumar Varshney  
**Department:** Electronics and Electrical Communication Engg.  
**Ref No.:** IIT/SRIC/R/ACG/2024/101  
**Ref Date:** 12-06-2024

**Project Title:** AI predictive model of climate change based on chip-scale gas monitoring sensors data(ACG)  
**Sponsor:** Department of Science and Technology (International Cooperation Division)(Government of India, Ministry of Science and Technology, Department of Science and Technology, Technology Bhavan, New Mehrauli Road, New Delhi-110 016)  
**Consolidated Compensation:** Upto Rs.37000.00(depending upon qualification & experience)  
**Qualifications:** M.Tech (CS/AI/ECE/EE/Instrumentation) or B.Tech (CS/AI/ECE/EE/Instrumentation) / M.Sc. (Physics) / M.Tech (Nano Science and Technology). Selected through a process described through any one of the following: a. Scholars who are selected through National Eligibility Tests – CSIR-UGC NET including Lectureship (Assistant Professorship) and GATE) b. The selection process through National level examinations conducted by Central Government Departments and their agencies and Institutions such as DST, DBT, DAE, DOS, DRDO, MHRD, ICAR, ICMR, IIT, IISc, IISER etc.  
**Relevant Experience:** Candidates with knowledge on metamaterials/metasurface and AI  
**Application Fees:** NIL  
**Age Limit:** Upper age limit is 28 years. Relaxable upto 5 years in the case of SC/ST/OBC/Physically Handicapped / Visually Handicapped and female applicant.  
**Tenure:** 24 Months or the termination of the project, whichever is earlier.

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**sition:** Research Associate  
**PI:** Shailendra Kumar Varshney  
**Department:** Electronics and Electrical Communication Engg.  
**Ref No.:** IIT/SRIC/R/ACG/2024/102  
**Ref Date:** 12-06-2024

**Project Title:** AI predictive model of climate change based on chip-scale gas monitoring sensors data(ACG)  
**Sponsor:** Department of Science and Technology (International Cooperation Division)(Government of India, Ministry of Science and Technology, Department of Science and Technology, Technology Bhavan, New Mehrauli Road, New Delhi-110 016)  
**Consolidated Compensation:** Upto Rs.58000(depending upon qualification & experience)  
**Qualifications:** -PhD (awarded/Completed) in the area of RF& Microwave, Photonics, or M.Tech (ECE/EE/Nanoscience and Technology/AI) having 3 Years of research, teaching and design and development experience with at least one research paper in Science Citation Indexed (SCI) journal. Candidate who have submitted the PhD Thesis may also apply, Selection in suchn cases will be subject to the condition that Ph.D award certificate should be produced at the time of joining.  
**Relevant Experience:** Candidate should have worked in the area of metasurface/metamaterials/photonics/RF& Microwave Candidate should be well-versed with the

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design and simulations along with hands-on experience with experiments

**Application Fees:** NIL

**Age Limit:** Upper age limit is 35 years. Relaxable upto 5 years in the case of SC/ST/OBC/Physically Handicapped / Visually Handicapped and female applicant.

**Tenure:** 24 Months or the termination of the project, whichever is earlier.

**Project Title:** Indigenous Development and Commercialization of Advanced Lightweight and Efficient Powertrain for Electric 3-wheelers(ICW\_2354\_TSA)

**Sponsor:** Ministry of Electronics and Information Technology(Government of India Electronics Niketan, 6, CGO Complex,Lodhi Road, New Delhi - 110003 )

**Consolidated Compensation:** Upto Rs.31000(depending upon qualification & experience)

**Qualifications:** 1. BE/B.Tech in Electrical /Electrical and Electronics /Electronics / Instrumentation/Robotics and Automation/ Electronics and Telecommunication/ Electronics and communication / Electronics and Instrumentation Engineering/Instrumentation and Control. Selected through a process described through any one of the following a. Scholars who are selected through National Eligibility Tests – CSIR-UGC NET including Lectureship (Assistant Professorship) and GATE) b. The selection process through National level examinations conducted by Central Government Departments and their agencies and Institutions such as DST, DBT, DAE, DOS, DRDO, MHRD, ICAR, ICMR, IIT, IISc, IISER etc.

**Relevant Experience:** Any one or more of the following (desirable but not mandatory): A. Hands on experience in embedded c coding for microcontroller (beyond Arduino such as 8051, TI, microchip, pic, etc) B. Power Electronic converter hardware development and testing C. BLDC Motor Control logic implementation in hardware D. M.Tech. qualified, specialization: Machine drives and Power Electronics OR Control System OR Instrumentation or Robotics or Mechatronics or Instrumentation and Control or equivalent Tenure: Till 22-12-2024 (may be extended) Salary: may be increased as per revised DST rate

**Application Fees:** NIL

**Age Limit:** Upper age limit is 28 years. Relaxable upto 5 years in the case of SC/ST/OBC/Physically Handicapped / Visually Handicapped and female applicant.

**Tenure:** 5 Months or the termination of the project, whichever is earlier.

**Position:** Junior Research Fellowship - Research

**PI:** Dipankar Debnath

**Department:** Electrical Engineering

**Ref**

**No.:** IIT/SRIC/R/ICW\_2354\_TSA/2024/102

**Ref Date:** 12-06-2024

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