

Office of the Dean Research and Consultancy
Indian Institute of Engineering Science & Technology (IIST), Shibpur
Howrah-711103

**Project on: "AI-Powered Robust VLSI Design and CAD for Microfluid Lab-on-Chips:
Empowering the Future of Semiconductor Eco-System Through Collaborative Research,
Innovation, and Mentorship for Societal Impact"**

[Sponsoring Authority: ANRF, Govt. of India]

School of VLSI Technology
Indian Institute of Engineering Science & Technology (IIST), Shibpur
Howrah-711103

Ref.: Advt. No. VL 1943, dated 23.07.2026

[University Project Code: DRC/ANRF (4211) / VLSI / BBB/004/26-27]

Interested candidates are requested to appear for walk in interview on **18.08.2026 at 11:00 AM**, for the following post in the **School of VLSI Technology, Indian Institute of Engineering Science & Technology (IIST), Shibpur, Howrah-711103**.

Name of the post: Senior Project Associate

No. of post – Two (02)

Essential Qualification: Ph.D (Awarded) in Engineering/Technology from a recognized Indian University/Institute with good number of research publication(s) in reputed journal.

Desirable Qualification:

2. At least 4 years of experience (research/professional) in VLSI Analog-Mix Signal/Quantum Computing.
3. Two published papers in SCOPUS/SCI/Web of Science/UGC Care listed or other reputed indexed journal.

Fellowship: Rs. 57,000/- per month (consolidated).

Age Limit: Maximum 45 years, (Upper age limit is relaxable up to 5 years for SC/ST/OBC/Woman and Physically handicapped candidates).

Duration of the Project: Up to 31st March 2027. May be renewed subject to availability of funds.

Interested eligible candidates should bring application letter in plain paper, recent bio-data, photo copies of the mark sheets, certificates, work experience certificate and a valid Identity proof. All documents should be self-attested. Documents will be verified at the time of joining. The selection will be cancelled if any discrepancies are found in the documents at the time of verification.

Venue and Date of the interview: 18.08.2026 School of VLSI Technology, IIST, Shibpur.

Note:

1. All applications must mention a valid e-mail id and phone number for further communication.
2. No TA/DA will be paid for attending the interview.

(W. Code DRC-024/26-27)

Nandadulal
Asst Dean (R & C) 27/7/26
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