# Department of Electronics Engineering, Aligarh Muslim University, Aligarh

## Advertisement for Junior Research Fellow

**Ref:** ELED/Adv./C2S/08/2025 , Dated: 25.11.2025

Applications are invited from Indian Citizens in the prescribed format for the temporary post of "Juniour Research fellow (JRF)" for the Project entitled "*Development of Silicon Proven IP Cores, Transceiver IC and System Prototype for mmWave Radar Sensing in Healthcare and Security Applications*". This is a joint project at **Aligarh Muslim University Aligarh** in collaboration with **IIIT Hyderabad and MNIT Jaipur**, and the selected candidate may be required to travel to these institutes. The selected candidate may also enroll in the Ph.D. Program in the **Department of Electronics Engineering, Aligarh Muslim University** as per the University norms and regulations.

| | | |
|---|---|---|
| 1. | Name of the Temporary Post | JRF – One in number |
| 2. | Name of the Research Project | Development of Silicon Proven IP Cores, Transceiver IC and System Prototype for mmWave Radar Sensing in Healthcare and Security Applications |
| 3. | Name of the Sponsoring Agency | Ministry of Electronics & Information Technology (MeitY), Government of India |
| 4 | Tenure of the Project | Project duration *or* up to 10.02.2027 (whichever is earlier) |
| 5. | Tenure of the Assignment | Initially for a period of one year or till the end of the financial year. (Annually extendable till the end of the project subject to satisfactory performance) |
| 6 | Job Description | To carry out research and development work on the topic realted to ML/Signal Processing/Algorithms implementation on Embedded/Edge devices and its integration with other Electronics systems/modules (but not limited to this work) |
| 7. | Consolidated monthly compensation/Fellows hip | For JRF: INR 37,000/- per month + 16% HRA |
| 8. | Essential Qualifications | For JRF: M. Tech in the field related to VLSI or Embedded Systems or Signal Processing or Electronics and Communication Engineering or Biomedical Engineering with a minimum 65% marks (for OBC/SC/ST 60% marks) and B.Tech. in the field related to Electronics or Communication or Computer Engineering with minimum 65% marks (for OBC/SC/ST 60% marks) |
| 9. | Desirable Qualifications | GATE/UGC-NET Qualified or good publication record |
| 10. | Mode of Selection | Candidate will be selected based on his/her performance in the interview. |

For technical information on the project, the candidate may contact the Chief Investigator (CI) at

Dr. Mohd Wajid, Department of Electronics Engineering, Aligarh Muslim University, Aligarh, UP 202002.
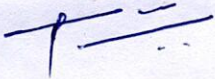+91-9218529591 mohdwajid@zhcet.ac.in

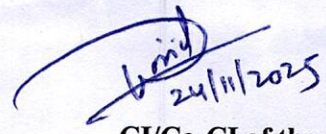**Tentative Date and Time for Interview:** 15/12/2025 at 10:30 am

**Place of Interview:** Dean's Office, Faculty of Engineering and Technology, AMU, Aligarh
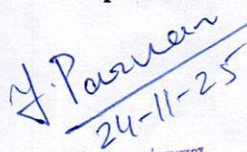
*Note: Interview date, time, venue and mode may get changed*

1. The eligible and interested applicants **must** send the complete filled-up application form (hard copy with coverpage where advertisement no. must be written) to the office of the **Chairperson, Department of Electronics Engineering, Aligarh Muslim University, Aligarh, 202002 U.P.** The application must arrive by **11 Dec, 2025 (during office hours)**. Candidate must also send the scanned softcopy of the application form along with supporting documents to **mohdwajid@zhcet.ac.in** with the subject line "*Application of JRF-C2S*"; however, typed signed hardcopy along with scanned supporting documents should be mandatorily received by **11 Dec, 2025 (during office hours)**.
2. The application form is attached with this notification (see pages 3 and 4) which must be typed and signed by the candidates. Self-attested photocopies of all supporting documents, research papers (if any), etc., must also be attached.
3. The candidates are also required to produce original documents mentioned in the application form [such as the original of all mark sheets and certificates, research papers (if any), experience certificate (if any), age proof, etc.] at the time of the interview and joining. Selection/Joining will be canceled in case of any suppression of information/document provided earlier.
4. Mere possession of the minimum qualification does not guarantee an invitation to the interview. Candidates may be shortlisted based on merit and need of the Project. The salary of the project staff shall be solely disbursed based on the funds received from MeitY for this Project.
5. The list of shortlisted candidates will be uploaded on the website of the Department of Electronics Engineering, AMU or may be intimated via email before 2 days of interview.
6. No TA/DA will be paid to the applicants called for an interview.
7. This assignment is purely contractual and may get terminated at any time upon unsatisfactory performance.
8. AMU or the Chief Investigator can cancel the recruitment or appointment for the aforesaid post at any time without providing any clarification/information.

**Dean,**
**Faculty of Engineering and Technology, AMU**

**CI/Co-CI of the project at AMU**
**Department of Electronics Engineering, AMU**

J. Parveen
24-11-25

Chairperson/अध्यक्ष
Dept. of Electronics Engineering
इलेक्ट्रॉनिक्स इंजीनियरिंग विभाग
A.M.U., Aligarh/एएमयू० अलीगढ़

Copy to:

1) All Heads of the Departments, ZHCET
2) CI, Co-CI, and other investigators with a request to give wide publicity to this advertisement.
3) Chairperson of the Department of Electronics Engineering, AMU
4) Academic Section, Registrar Office, AMU
5) Project file.
6) Notice Board
7) Website

# Department of Electronics Engineering, Aligarh Muslim University, Aligarh

## Application Form

**Ref:** ELED/Adv./C2S/08/2025 , Dated: 25.11.2025
**The application form should be neatly typed**

1. Post applied for (write Junior Research Fellow-JRF):

   a). _____

<div style="border:1px solid; padding:8px; float:right">
Affix a recent passport size photograph (Self-Attested)
</div>

2. Name of the Applicant:

3. Father's Name:

4. Mother's Name:

5. Date of Birth of the Applicant:

6. Gender:

7. Marital Status:

8. Category (General/SC/ST/OBC/Other):

9. GATE/NET Qualified in *Electronics and\or Communication Engg.*: (Yes/No), if yes give details also:

10. Contact Details:

    a. Mailing Address:

    b. Permanent Address:

    c. Mobile Number:

    d. Email ID:

11. Educational Qualifications:

| Qualification | Board/University/Institute | Year of Passing/Qualifying | Percentage of Marks Obtained | Subjects |
|---|---|---|---|---|
| Class X | | | | |
| Class XII | | | | |
| Diploma Engg | | | | |
| B Tech/BE. | | | | |
| M Tech/ME | | | | |
| GATE/NET Qualified in *Electronics and\or Communication Engg.* | | | | |

*(Attached the self-attested educational certificates, Marksheets, Gradesheets, etc.)*

12. Work Experience (if any)

| Designation | Name of Organization | Nature of Work | Working Duration | |
|---|---|---|---|---|
| | | | From | To |
| | | | | |
| | | | | |
| | | | | |

*(Attached the self-attested appointment letters, experience certificates, age proof etc.)*

13. List of Publication (if any)

14. Area of Interest:

15. Any other relevant information

16. List of enclosures:

Declaration: I hereby declare that I have carefully read and understood the instructions and particulars of this application and that all entries in this form as well as in the attached sheets are true to the best of my knowledge and belief

Place:

Date:                                                      Applicant's Signature