इंडियन इंस्टीट्यूट ऑफ टेक्नोलॉजी दिल्ली

हौज खास, नई दिल्ली -110016

(औद्योगिक अनुसंधान एवं विकास इकाई)

INDIAN INSTITUTE OF TECHNOLOGY DELHI

Hauz Khas, New Delhi-110016

(Industrial Research & Development Unit)

No. IITD/IRD/MI00336G/ 111245   Dated: 09/01/2023

Advertisement No.: IITD/IRD/009/2023

Applications from Indian nationals are invited for Project Appointment under the following project. Appointment shall be on contractual basis with consolidated pay, renewable yearly or upto the duration of the project, whichever is earlier. निम्नलिखित परियोजना के तहत भारतीय नागरिकों से आवेदन आमंत्रित किए जाते हैं। अपॉइंटमेंट, अनुबंधित आधार पर समेकित वेतन, नवीकरणीय वार्षिक या परियोजना की अवधि तक, जो भी पहले हो, के साथ होगा।

Title of the Project	Setting up the Information Technology School at IIT Delhi (MI00336G)	
Funding Agency	**School of Information Technology, IIT Delhi**	
Name of the Project Investigator	Prof. Kolin Paul [email id:kolin@cse.iitd.ac.in[	
Deptt/.Centre	Department of Computer Science & Engineering	
Duration of the Project	Upto:31/08/2024	
Post (s)	Consolidated fellowship / Pay-slab	Qualifications
Principal Project Scientist (1)	Rs.56,000-60,000-64,000-69,000-74,000-79,000/- p.m. plus HRA @ 24%	Ph.D. with first class or equivalent at the preceding degree in CS/EE or related areas OR M. Tech. 1st class or equivalent with 6 years experience. At least 2 research papers published in IEEE or ACM journals and conferences in embedded systems-related areas. The project involves temperature-aware performance optimizations in embedded systems based on emerging memory technologies. The project also requires the employee to design and implement strategies for managing shared caches. The algorithms would be implemented in multiprocessor simulators such as gem5 and Sniper. A strong background in cache memory, CPU and memory simulators, and computer architecture is essential. The following technical background is required: (1) Knowledge of embedded system design principles, hardware-software co-design, and energy efficiency computing issues. (2) Experience in processor and memory simulators (3) Fluency in C++/Python/VHDL/Verilog.

The candidates who are interested to apply for the above post should download **Form No. IRD/REC-4** from the IRD Website **(http://ird.iitd.ac.in/rec)** of IIT Delhi and submit the duly filled form with complete information regarding educational qualifications indicating percentage of marks/division, details of work experience etc. **by e-mail with advertisement No. on the subject line to Prof. P.R. Panda at email id**:panda@cse.iitd.ac.in OR **by post to Prof. P.R. Panda, School of Information Tehcnology IIT Delhi, Hauz Khas, New Delhi-110016** The advertisement No. & post applied for must be mentioned on the left side of the application.

IIT Delhi reserves the right to fix higher criteria for short-listing of eligible candidates from those satisfying advertised qualification and requirement of the project post and their name will be displayed on web link **(http://ird.iitd.ac.in/shortlisted) alongwith the online interview details. Only short-listed candidates will be informed for online interview.** In case any clarification is required on eligibility regarding the above post, the candidate may contact **Prof. Kolin Paul at email id: kolin@cse.iitd.ac.in**

5% relaxation of marks may be granted to the SC/ST Candidates. In case of selection of a retired/superannuated government employee, his/her salary will be fixed as per prevailing IRD norms. अनुसूचित जाति / अनुसूचित जनजाति के उम्मीदवारों को अंकों की 5% छूट दी जा सकती है. एक सेवानिवृत्त सरकारी कर्मचारी के चयन के मामले में उसका वेतन वर्तमान आईआरडी मानदंडों के अनुसार तय किया जाएगा। **The last date for submitting the completed applications by e-mail or by post is 23/01/2023 by 5.00 p.m.**

10.01.2023

सहायक कुलसचिव, आईआरडी

वितरण

- Head of the Deptt./Centres/Units :   It is requested that the contents of the Above Advt. be brought to the notice of the staff working in your Deptt./Centre/Unit
- Webmaster, IRD   :   To put advertisement at IITD website.
- Notice Boards
- Advertisement file
- Prof. Kolin Paul, PI, Department of Computer Science & Engineering
- Prof. Preeti Ranjan Panda, Department of Computer Science & Engineeering
- Copy to Chairperson, DRC/CRC